

100_004 Sym-Enc-CBC-CTR Page 1

1. Electronic Code Book (ECB) mode in AES-128

This is considered to be the easiest block cipher mode of operation. In electronic codebook mode (ECB) the plain text is divided into the blocks, each of 128-bit. Each block is encrypted one at a time to produce the cipher block. The same key is used to encrypt each block.

When the receiver receives the message i.e. ciphertext. This ciphertext is again divided into blocks, each of 128-bit and each block is decrypted independently one at a time to obtain the corresponding plain text block. Here also the same key is used to decrypt each block which was used to encrypt each block.



 $EncAES(k, P_1) = C_1$ $ENCAES(k, P_2) = C_2$ $ENCAES(k, P_n) = C_n$



(a) plaintext



(b) plaintext encrypted in ECB mode using AES



Original image



Encrypted using ECB mode

Modes other than ECB result in pseudo-randomness

https://binaryterms.com/block-cipher.html

2. Cipher Block Chaining - CBC Mode

To overcome the limitation of ECB i.e. the repeating block in plain text produces the same ciphertext, a new technique was required which is Cipher Block Chaining (CBC) Mode. CBC confirms that even if the plain text has repeating blocks its encryption won't produce same cipher block.

To achieve totally different cipher blocks for two same plain text blocks **chaining** has been added to the block cipher. For this, the result obtained from the encryption of the first plain text block is fed to the encryption of the next plaintext box.

In this way, each ciphertext block obtained is dependent on its corresponding current plain text block input and all the previous plain text blocks. But during the encryption of first plain text block, no previous plain text block is available so a random block of text is generated called **Initialization vector**.

Now let's discuss the encryption steps of CBC

Step 1: The initialization vector and first plain text block are XORed and the result of XOR is then encrypted using the key to obtain the first ciphertext block.

Step 2: The first ciphertext block is fed to the encryption of the second plain text block. For the encryption of second plain text block, first ciphertext block and second plain text block is XORed and the result of XOR is encrypted using the same key in step 1 to obtain the second ciphertext block.

Similarly, the result of encryption of second plain text block i.e. the second ciphertext block is fed to the encryption of third plain text block to obtain third ciphertext block. And the process continues to obtain all the ciphertext blocks.

Decryption Steps:

Step 1: The initialization vector is placed in the shift register. It is encrypted using the same key.

Keep a note that even in the **decryption process** the **encryption** algorithm is implemented instead of the decryption algorithm.

Then from the encrypted IV s bits are XORed with the s bits ciphertext C1 to retrieve s bits plain text P1.

Step 2: The IV in the shift register is left-shifted by s bits and the s bits C1 replaces the rightmost s bits of IV.

CBC		
Cipher block chaining		
Encryption	No	
Decryption	Yes	
Random read	Yes	
access:		

The process continues until all plain text fragments are retrieved.



$ \oplus 1010 \\ \oplus 1111 \\ \oplus 1011 \\ \oplus 1011 \\ \oplus 1011 \\ \oplus 1001 \\ \oplus 1001 $	$C_1 \oplus k \oplus lv = P_1 \oplus lv \oplus k \oplus C_1 \oplus k \oplus lv$			
1111 0010 1001 0100	$= P_1 \oplus V \oplus V \oplus K \oplus K \oplus K = P_1 \oplus \mathcal{O} \oplus \mathcal{O}$			
(D 1010) (D 1000)	€ 0101 € 1101 € 1010			
$\frac{0010}{1000} = \frac{1101}{0101}$	$\frac{0101}{0000} = \frac{1001}{0000} = \frac{0000}{1010} = A$			
https://binaryterms.com/block-cipher.html				
5. Counter Mode - CTR				
It is similar to OFB but there is no feedback mechanism in counter mode.				
Nothing is being fed from the previous step to the next step instead it uses a				
sequence of number which is termed as a counter which is input to the encryption function along with the key. After a plain text block is encrypted the				
counter value increments by 1.				
Steps of encryption:				
<i>Step1:</i> The counter value is encrypted using a key.				
<i>Step 2:</i> The encrypted counter value is XORed with the plain text block to obtain a ciphertext block.				
To encrypt the next subsequent plain text block the counter value is				
incremented by 1 and step 1 and 2 are repeated to obtain the corresponding				
cipnertext.	СТР			
The process continues until all plain text block is encr	/pted.			
Steps for decryption:	Encryption Yes			
Step1: The counter value is encrypted using a key. parallelizable:				
Note: Encryption function is used in the decryption provide the state of the sector	ocess. The same counter parallelizable: Yes			
Step 2: The encrypted counter value is XORed with th	Random read Yes			
obtain a plain text block.				
$00 \dots 01$ $00 \dots -10$ $11 \dots 11$	0001 0010 MM			
K K K K K K K K K K K K K K K K K K K				
Key Encryption Key Encryption Key Encryption Key Encryption				
$P1 \longrightarrow E1 \qquad P2 \longrightarrow C2 \longrightarrow E1 \qquad C1 \longrightarrow E1 \qquad C2 \longrightarrow E2 \qquad Cn \longrightarrow En \qquad$				

C2

C1

 $C_1 = P_1 \oplus E_1$

 $C_1 \oplus E_1 = P_1 \oplus E_1 \oplus E_1 = P_1 \oplus \mathcal{O} = P_1$

Cn

P2

P1

Pn



$$r_{i+1} = PRNG(r_i) \quad r_i = PRNG(r_i); \quad r_o = initial value: r_o = k.$$

$$r_s = PRNG(r_s) \quad Nvn-lineat dignarmic systems$$

$$r_n = PRNG(r_{n-1})$$

$$r_n = PRNG(r_{n-1})$$

$$r_n = PRNG(r_{n-1})$$

$$r_n = r_n = r_n$$

$$Analog code converter \quad r_{1PS-140-2}$$

$$r_n = r_i = r_i = r_i = r_i$$

$$r_n = r_i = r_i = r_i$$

$$r_n = r_i = r_i = r_i$$

$$r_n = r_i = r_i$$

$$r_n = r_i = r_i$$